Final exam Electronics & Signal processing 10-04-2018 Prof. Dr. G. Palasantzas

Grade of written exam: Mark is the cummulative points scored for all problems Total maximum score : 10

Problem 1 (1.5 points)



Derive the corresponding Thévenin equivalent and calculate:

(a: 1 point) The venin voltage V_{Th}

(b: 0.5 points) Thevenin resistance RTh



substitution gives

$$P_{-}^{+} O \left(V_{1} + LR_{1} \right) \frac{R_{2}}{R_{1} + R_{2}} P_{1} + \int V_{1} + V_{2} + \int V_{1} + V_{2} +$$

Problem 2 (2.5 points)

(a: 1 point) Consider the negative feedback circuit shown below with an ideal opamp so that V + - V = 0 (assume for the opamp infinite input and zero output resistances)..



(b: 1.5 points) Consider the negative feedback circuit shown below with an opamp of finite forward gain A so that Vo=A(V+ - V-) (assume for the opamp infinite input and zero output resistances).



(a) Consider the K-law for currents



R=R1//(R3+R4)(Vi-Vc)/R2=[(Vc-Vo)/R] (1) (Vc-0)/R3=(0-Vo)/R4 (2) From (2) Vc=-Vo(R3/R4) and substitution in (1) gives

(Vi/R2) + Vo(R3/R4R2) = -Vo(1+R3/R4)/R

 $Vo\{(R3/R4R2)+(1+R3/R4)/R\}=-Vi/R2$

 $Vo = -Vi / \{ (R3/R4) + (1 + R3/R4)R2/R \}$



 $V_{-} = V_{0} \frac{R_{1}}{R_{1} + R_{2}} (1) \ 0.5 p$ $I_{0} = I_{3} + I_{6} (2)$ I I I I I $\frac{V_{0} - V_{1}}{R_{5}} \frac{V_{1} - V_{5}}{R_{3}} \frac{V_{1} - 0}{R_{6}}$

$$\begin{array}{l} (2) = \wp \quad \frac{V_0 - V_1}{R_5} = \frac{V_1 - V_2}{R_3} + \frac{V_1 - 0}{R_6} = \wp \\ = \wp \quad \frac{V_0}{R_5} + \frac{V_5}{R_3} = V_1 \left(\frac{1}{R_5} + \frac{1}{R_3} + \frac{1}{R_6}\right) = \\ & 1/\tilde{\rho} \\ = \wp \quad V_1 = V_0 \quad \frac{\tilde{\rho}}{R_5} + V_3 \quad \frac{\tilde{\rho}}{R_3} \quad (3) \quad 0.5 p \\ \end{array}$$

 $\frac{V_0}{n} = V_+ - V_-$



 $V_0(\frac{1}{A} - \frac{R}{RS} + \frac{R_I}{R_I + R_Z}) = V_S \frac{\tilde{R}}{R_Z} = P$

 $V_0 = V_S \frac{R}{R_3} \left(\frac{1}{A} - \frac{R}{R_5} + \frac{R_1}{R_{11} + R_2} \right)$

Problem 3 (1.5 points)

Consider the circuit shown below. The diode is ideal with forward conduction voltage V_c (assume for the applied potential V>0).



(a: 1 point) Calculate the current via the resistor R₃

(**b: 0.5 points**) Calculate the current through the diode when it conducts current.

(a) In absence of the diode the volage difference V12=V1-V2=V(R2/R) with R=R1+R2+R3

<u>Case 1</u>: If V12 \geq Vc then the diode conducts

(V-V1)/R1=V2/R3, V1-V2=Vc or V1=V2+Vc

(V-Vc)/R1 = V2/(R1//R3) thus

V2=(V-Vc)[(R1//R3)/R1] so we obtain

I3= V2/R3= (V-Vc) [(R1//R3)/R3R1] or <u>I3=(V-Vc)/(R3+R1)</u>

<u>Case 2</u>: If V12<Vc then the diode does not Conduct so that we have I3=V/R

(b) When diode conducts current ID ($\underline{a: case 1}$) ID+(Vc/R2)=I3 thus we have

ID = (V-Vc)/(R3+R1) - (Vc/R2)

Problem 4 (1.5 points)

(a: 0.5 point) -The Nyquist diagrams below represent two circuits. Determine the number of low-frequency and high-frequency cutoffs and indicate which system is stable



(**b:1 point**) Consider the opamp to have has infinite input and zero output resistance



Assume R1=6 K Ω , R2=30 K Ω , and input potential Vi= Voi $\sin(\omega t)$ with Voi= 5 V

Draw the output potential Vu and justify briefly your answer

3 high cut offs al low cut-off (a) Cinstable

I high cut-off 3 la cut-offs (b) stable

(b) Positive feedback \rightarrow oscillation between -15 and +15 V



Problem 5 (1.5 points)

Design a synchronous counter (using J-K flip flops) that counts through the states 0, 4, 2, 1, 5, 3.

Q _{n-1}	Q _n	J	К
0	0	0	*
0	1	1	*
1	0	*	1
1	1	*	0

*: don't care

J	К	Q _n
0	0	Q _{n-1}
0	1	о
1	0	1
1	1	Q _{n-1}



Problem 6 (1.5 points)

Consider a FET amplifier as it is shown bellow:



Calculate the amplification ratio U_o / U_i

Consider as known for the FET the transconductance g_m , and the differential resistance r_d when the FET operates at saturation.

(2-method: small signal cicuit) This is for normal cookies!

Replace in all shown bellow: RD with RD//RL. This is because in this design RD parallel with RL



$$g_{m}v_{gs} + (v_{o} - v_{s})/r_{d} - v_{s}/R_{s} = 0 \quad (S)$$

$$(v_{o}/R_{D}) + (v_{s}/R_{s}) = 0 \quad (E)$$

$$v_{gs} = v_{g} - v_{s}$$

$$\downarrow$$

$$v_{g} = v_{i}$$
Gain: $v_{o}/v_{i} = -g_{m}R_{D}/[1 + g_{m}R_{s} + (R_{s} + R_{D})/r_{d}]$

Replace in all shown bellow: RD with RD//RL. This is because in this design RD parallel with RL

(2-method: first priciples analysis) This is only for tough cookies: so you do it or you do not do it!





substitute in (4) from (5) the Us and replace also Ug=Ui, Ud=Vo

$$U_{o}\left(1+\frac{Rp}{PL}\right) = -\left[g_{m}\left(U_{i}+U_{o}\frac{Rs}{RpL}\right)+U_{o}\frac{1+\frac{Ks}{RpL}}{V_{i}}\right]Rp$$

$$U_{o}\left(\frac{1}{Rp}+\frac{1}{PL}\right) = -g_{m}U_{i} - g_{m}U_{o}\frac{Rs}{PpL} = U_{o}\frac{RpL+Rs}{V_{i}RpL}$$



Although this looks complicated, this is what is happening in reality!

you can extend this approach beyond first order perturbation theorya limitation for method-1.